

Radiofrequency frequency multiplier comprising an automatic level control circuit

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Radiofrequency frequency multiplier comprising an automatic level control circuit consisting of a microwave MESFET (T1) used as an oscillator signal distorter amplifier followed by a band-pass filter (FPB) tuned to the desired harmonic. The power of the harmonic is stabilized by means of a control circuit (CONTR) placed between the gate (G) and drain (D) terminals of the MESFET. Said control circuit operates as a feedforward controller varying the polarization voltage V_{DS} of the MESFET with the variation in the power level of the oscillator signal in accordance with a law which holds constant the power transferred from the MESFET to the selected output harmonic.

